



First Named Inventor	Zahilya Austin
Appln. No.	10/662,748
Filed	September 15, 2003
Title	INTEGRATED CIRCUIT OSCILLATOR
Docket No.	M923.12-0001

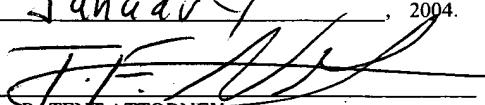
Group Art Unit: 2817  
Examiner:

### INFORMATION DISCLOSURE STATEMENT

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I CERTIFY THAT THIS PAPER IS BEING SENT BY U.S. MAIL,  
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PATENT ATTORNEY

Sir:

The enclosed PTO Form-1449 lists patents and publications submitted pursuant to 37 C.F.R. 1.97. Copies of the patents or publications are enclosed as necessary [copies of U.S. patents and application publications are not required if application filing date is after June 30, 2003].

### TIME OF FILING

The Information Disclosure Statement is being filed with the application or within three months of the filing date of a national application (other than a continued prosecution application under 37 C.F.R. 1.53(d)) or date of entry into the national stage of an international application or, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits or a first office action after the filing of a request for continued examination under 37 C.F.R. 1.114, whichever event occurs last. In accordance with 37 C.F.R. 1.97(b), no certification or fee is required.

Respectfully submitted,

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FORM PTO-1449	FEB 02 2004 PATENT & TRADEMARK OFFICE	Atty. Docket No.: M923.12-0001	Application No.: 10/662,748
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		First Named Inventor: Zahilya Austin	
		Filing Date: September 15, 2003	Group Art: 2817

## U.S. PATENT DOCUMENTS

Examiner Initials	Document No.	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents
AA	6,559,024.B1	05-06-2003	Boles et al.
AB	6,208,214 B1	03-27-2001	Geddes et al.
AC	5,546,056	08-13-1996	Tokumitsu
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AH			
AI			

## FOREIGN PATENT DOCUMENTS

		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Translation Yes No
	AJ				
	AK				

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AL	A. Boudiaf, M. Ahdjoudj, and P. Pouvil. Low Phase-Noise PHEMT-Based MMIC VCOs for LMDS Applications. <i>IEEE 2001 MTT-S Int'l. Symposium Digest</i> , Vol. III, pp. 1559-1562.
AM	P.J. McNally, T. Smith, F.R. Phelleps and K.M. Hogan. K <sub>u</sub> - and K-Band GaAs MMIC Varacter-Tuned FET Oscillators Using MEV Ion-Implanted Burried-Layer Back Contacts. <i>IEEE 1990 Microwave and Millimeterwave Monolithic Circuits Symposium Digest</i> , pp. 107-110.
AN	E. Reese, Jr. and J.M. Beall. Optimized X & K <sub>u</sub> Band GaAs MMIC Varactor Tuned-FET Oscillators. <i>IEEE MTT-S International-Microwave Symposium Digest</i> , Vol. 1, pp. 487-490.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.